



Date of Notice

of Allowance:

September 22, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Vickie D. Wall

Applicant

Janardhanan S. Ajit

Confirmation No. 7260

Application No.

10/043,763

Filed

January 9, 2002

Title

SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT

OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE

POWER SUPPLY VARIATIONS

Grp./Div.

2816

Examiner

Terry D. Cunningham

Docket No.

: 41980/RJP/B600

AMENDMENT UNDER 37 CFR § 1.312

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Post Office Box 7068 Pasadena, CA 91109-7068 December 2, 2004

Commissioner:

Initially, the Applicant would like to express its gratitude toward the Examiner for taking the time to discuss the present application by telephone on November 30, 2004 prior to making this submission. The amendments herein reflect the subject matter discussed by telephone with the Examiner. Specifically, while reviewing the case file after receiving the Notice of Allowance, it was noted by the Applicant that the drawings filed on August 2, 2002, were acknowledged in the Notice of Allowance. However, the drawings filed on January 30, 2003 and on March 2, 2004, were not acknowledged, copies of which are enclosed for the Examiner's convenience, in addition to a complete set of replacement drawings.

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Amdt date December 2, 2004
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Additionally, as discussed with the Examiner on November 30, 2004, the Applicant has amended FIG. 6 and FIG. 12B to correct certain minor typographical mistakes. No new matter has been added.

The Applicant respectfully requests entry of this amendment prior to issuance of the above-referenced application into a patent.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

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PCH/vdw

Enclosures:

Replacement Sheets (23 sheets)

Annotated Sheets Showing Changes (8 sheets)

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